

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 89518
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Mou-Shiung Lin, et al.	:	Confirmation Number: 8665
	:	
Application No.: 10/755,042	:	Group Art Unit: 2815
	:	
Filed: January 09, 2004	:	Examiner: JEROME JACKSON, JR.
	:	
For: INTEGRATED CHIP PACKAGE STRUCTURE USING SILICON SUBSTRATE AND METHOD OF MANUFACTURING THE SAME		

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being electronically-transmitted to
the United States Patent and Trademark Office on **December 13, 2010.**
/ShirLynn Mata/

ShirLynn Mata

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Out of an abundance of caution and in keeping with Applicants' obligation to bring possibly relevant material to the attention of the examiner in this pending application, Applicants cite these references in this IDS for the examiner's consideration.

10/755,042

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing of a Final Rejection or Notice of Allowance. Please charge the fee of \$180.00 to Deposit Account No. 502624.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 502624 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

/Dennis A. Duchene/
Dennis A. Duchene
Registration No. 40,595

11682 El Camino Real, Suite 400
San Diego, CA 92130
Phone: 858.720.3300 DAD:MWE
Facsimile: 858.720.7800
Date: December 13, 2010

**Please recognize our Customer No. 89518
as our correspondence address.**